





- Details will be e-mailed the day after the class ends
- Take the exam in any 3 hour period from the day after class ends to seven days thereafter
- <u>When you're ready</u>, request the exam with e-mail to exam@hte.com with "ecp1final" on the subject line
- You will receive the exam in PDF and Word DOC format within a few minutes
- You have 3.5 hours total: 3 to take the exam and 30 minutes to acquire and return it via fax or e-mail

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- ECP II: Embedded Programming in C
- ECP III: Real Time Programming
- Embedded Real Time Operating Systems I, II
- Embedded Interfacing •
- Embedded Linux .
- Embedded USB •

- Hardware:
  - Intro to Embedded Systems: Electronics and **Digital Logic**
  - Embedded Controller • Hardware Design (live and on-line)
  - High Speed Digital Design

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- Programmable Hardware Design
- VHDL/Verilog

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State Mode			
Analyzer Markers Off	Listing Acq 07 Ja	68360 asyn (Invasm Options) (Car uisition Time n 1994 07:57:27	Run
Label> Base> 61 65 67 68 75 76 80 81 87 89 90 92 92 95 101 103	ADDR [] Hex [] 0003FB4 0003FB8 04012B0 04012B0 040129C 0002A18 040129C 0002A1C 0002A22 040147C 0002A22 040147C 0002A22 040147C 0002A22 0401298 0002A22 0002A22 0002A22	68360 DATA Bus 0 = hex, 10. = decimal MOVE.L (0008,A6),-(A7) JSR 00002A18 00401B40 supr data read 00401B40 supr data write 00003FBE supr data write LINK.W A6,#0000 004012AB supr data write MOVEA.L 0040147C,A0 MOVE.N (A0),D0 00022000 supr data read BTST #15.,D0 A000xxxx supr data read BE0.B 00002A2E MOVEA.L *******,A0 UNLK A6	FC Symbol spgm 6 sdata 5 sdata 5 sdata 5 sdata 5 spgm 6 sdata 5





































